


PG611 A00

12GB GDDR5X, 384b, 256Mx32
TALL DVI-D + DP + DP + HDMI/DP + DP

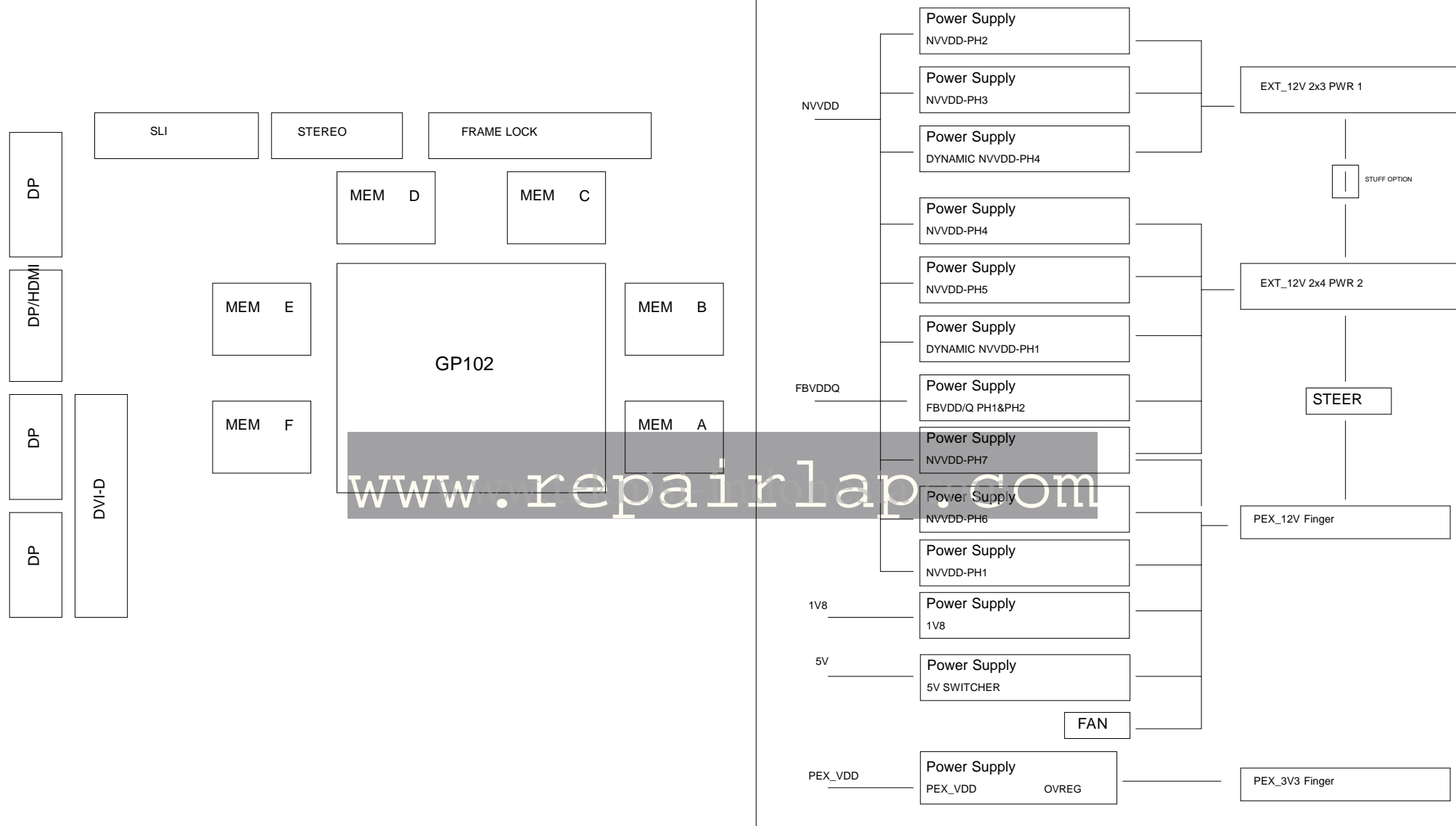
TABLE OF CONTENTS

Page	Description	Page	Description
1	Table of Contents	26	MIOA/B INTERFACE & FRAME LOCK
2	BLOCK DIAGRAM	27	MISC: FAN, THERMAL, JTAG, GPIO, STEREO
3	PCI EXPRESS	28	MISC: ROM, STRAPS
4	MEMORY: GPU PARTITION A/B	29	MISC: XTAL, PLL
5	MEMORY: FBA PARTITION[31:0]	30	PS: 5V, PEXVDD
6	MEMORY: FBA PARTITION[63:32]	31	PS: 1V8 Rails
7	MEMORY: FBB PARTITION[31:0]	32	PS: FBVDDQ
8	MEMORY: FBB PARTITION[63:31]	33	PS: NVVDD Controller_OVR8
9	MEMORY: GPU PARTITION C/D	34	PS: NVVDD Controller_PWR-MODULE
10	MEMORY: FBC PARTITION[31:0]	35	PS: NVVDD Phase 1, 2
11	MEMORY: FBC PARTITION[63:32]	36	PS: NVVDD Phase 3, 4
12	MEMORY: FBD PARTITION[31:0]	37	PS: NVVDD Phase 5
13	MEMORY: FBD PARTITION[63:32]	38	PS: NVVDD Phase 6, 7
14	MEMORY: GPU PARTITION E/F	39	PS: Dynamic power balance phase
15	MEMORY: FBE PARTITION[31:0]	40	PS: Dynamic power balance logic
16	MEMORY: FBE PARTITION[63:32]	41	PS: Input, filtering, and Monitoring
17	MEMORY: FBF PARTITION[31:0]	42	PS: Current Sterring, Hot Unplug
18	MEMORY: FBF PARTITION[63:32]	43	PS: NVVDD ENABLE
19	GPU PWR AND GND	44	PS: GC6 MISC
20	GPU DECOUPLING	45	GEFORCE LED AND SLI LED
21	IFPAB DVI-D-DL	46	PS: NV3V3, NV12V
22	IFPE DP	47	PS: MCU
23	IFPF DP	48	MECH
24	IFPC HDMI/DP	49	VR Thermal Protection
25	IFPD DP		

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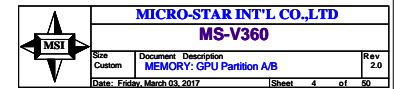
			
MICRO-STAR INT'L CO.,LTD MS-V360			
Size	Document	Description	Rev
Custom	Table of Contents		2.0
Date: Friday, March 03, 2017			Sheet 1 of 50

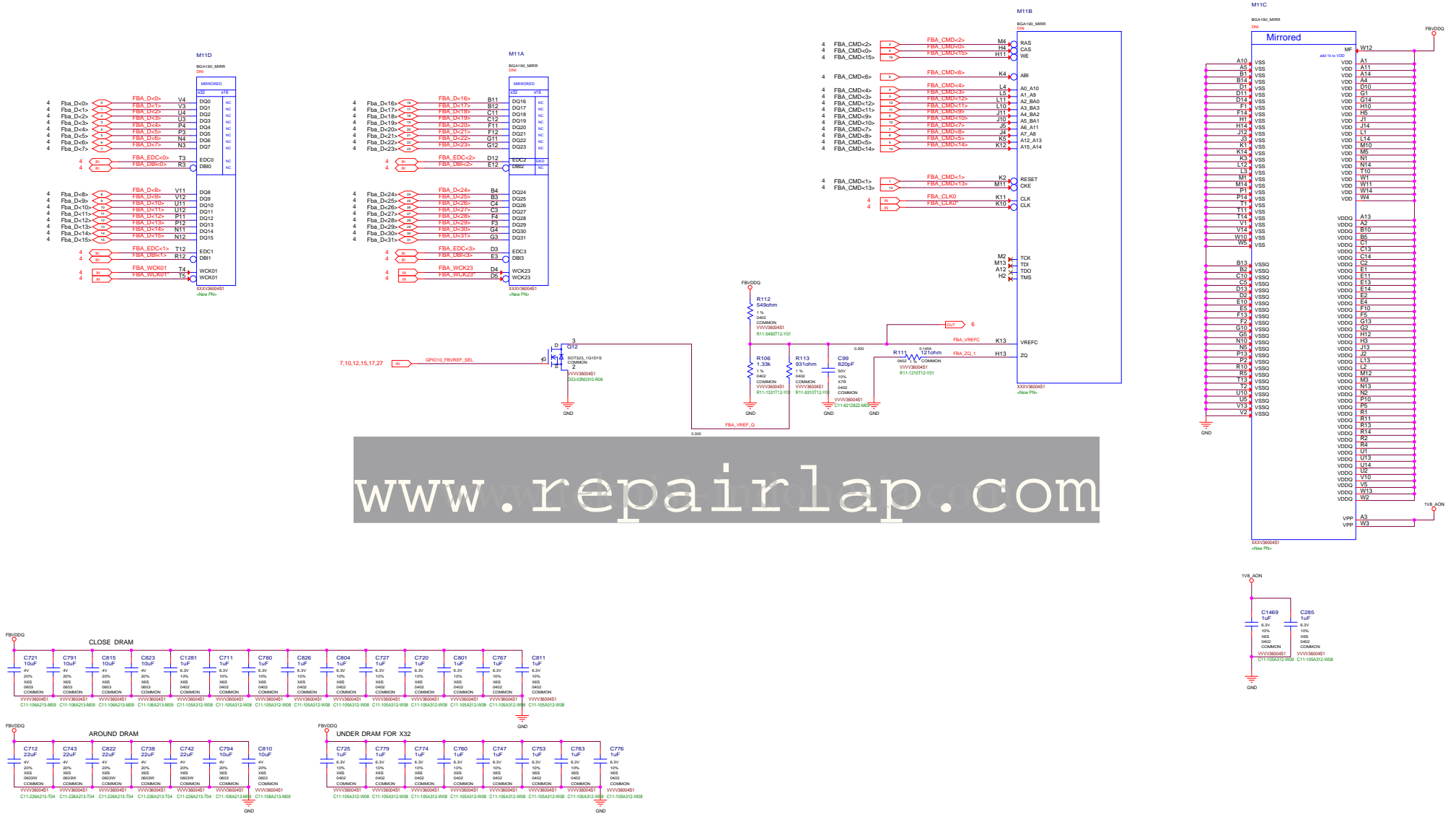


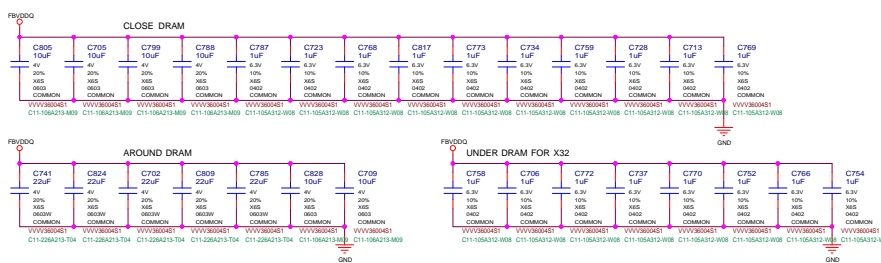
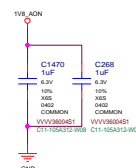
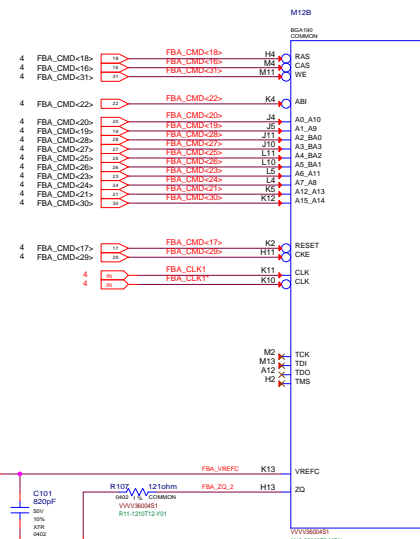


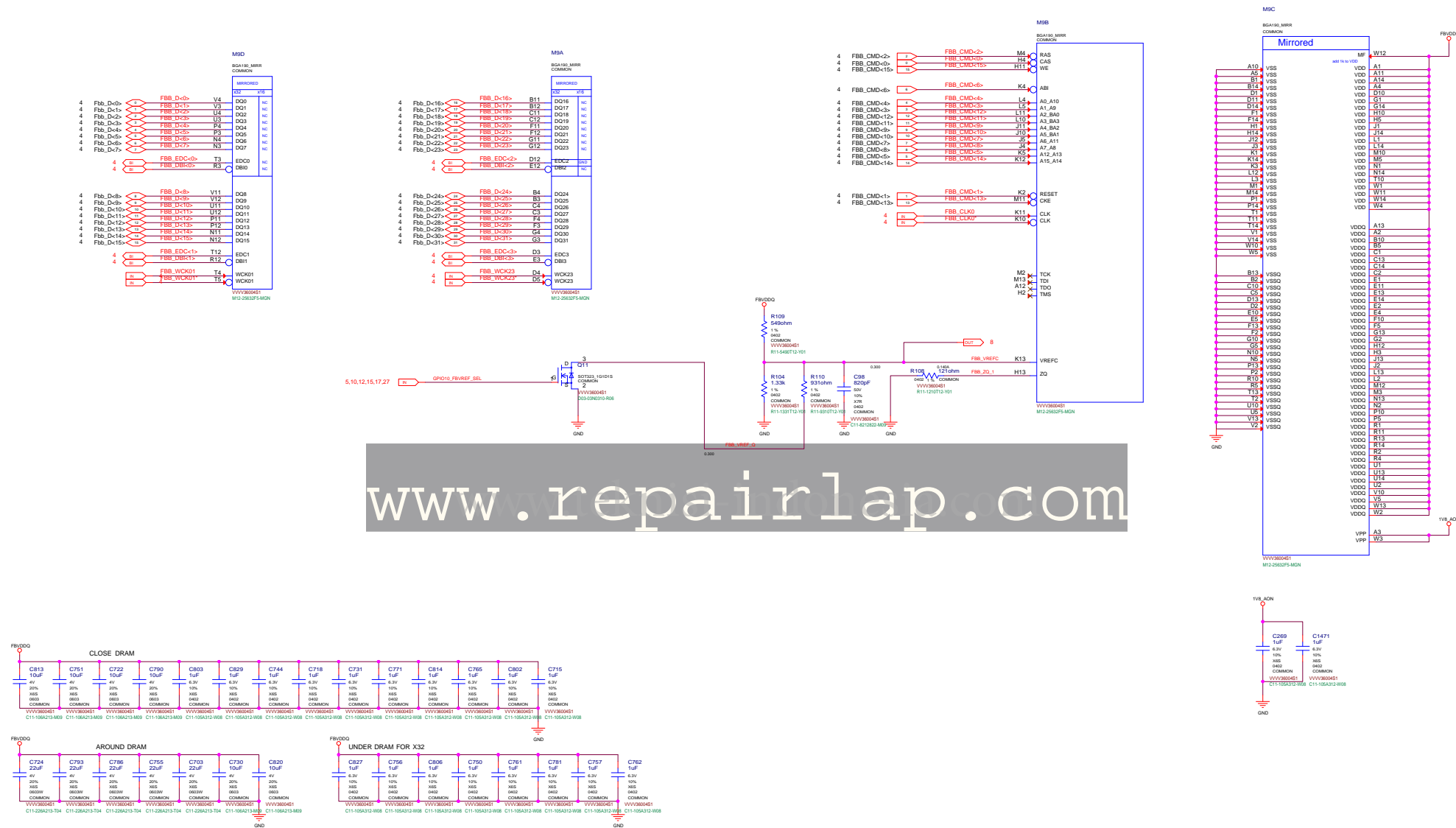
MICRO-STAR INT'L CO.,LTD			
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Size	Document	Description	Rev
Custom	Block Diagram		2.0
Date: Friday, March 03, 2017	Sheet	2	of 50

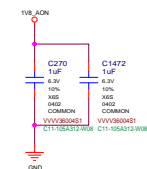
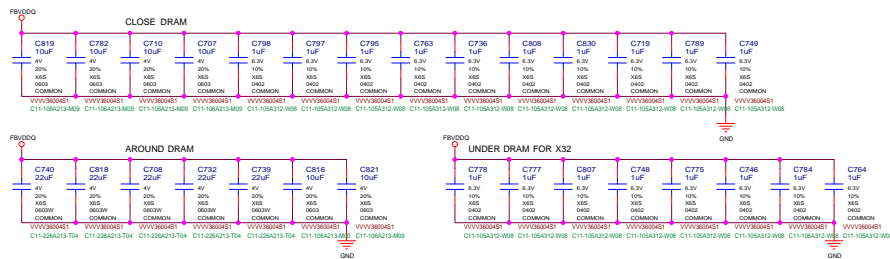
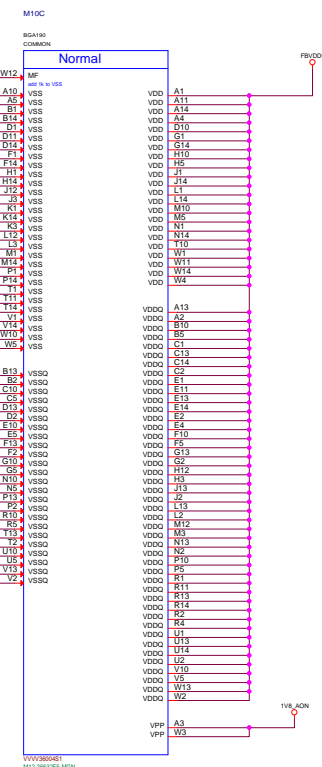
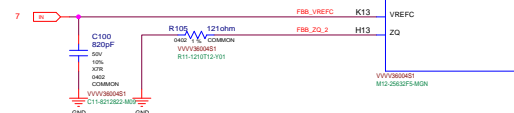
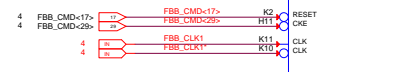
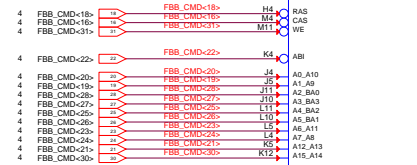
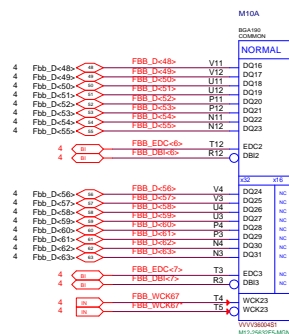




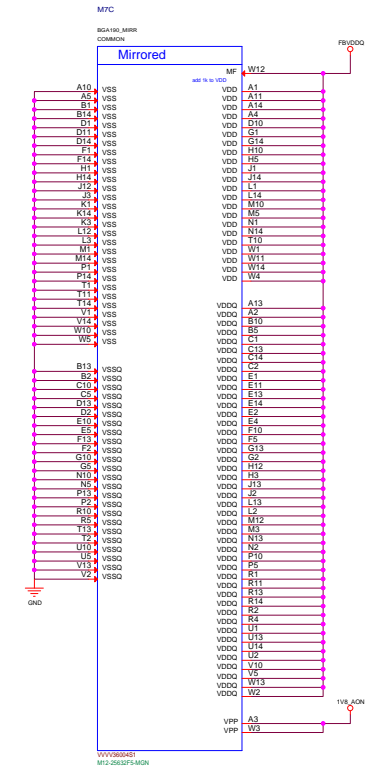
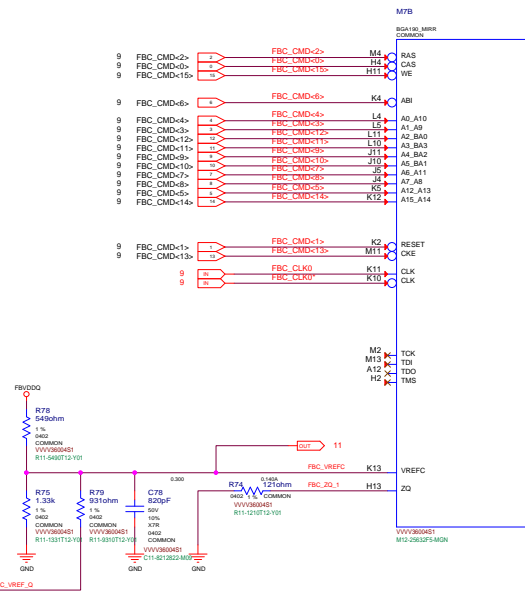
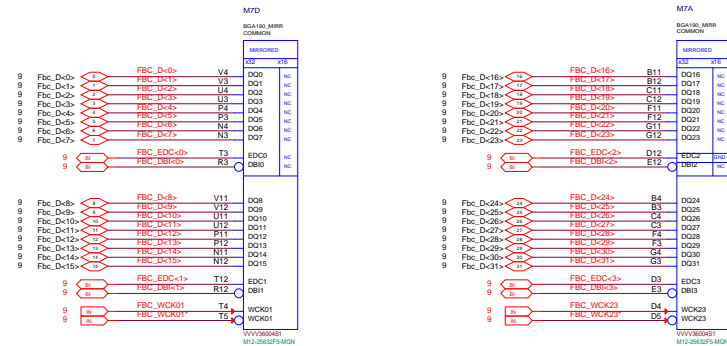




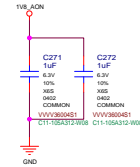
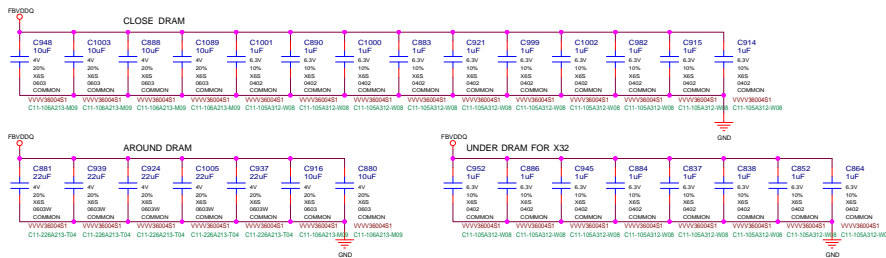




Size Custom	Document Description MEMORY: FBB Partition 63..32	Re 2
Date: Friday, March 03, 2017		Sheet 8 of 50

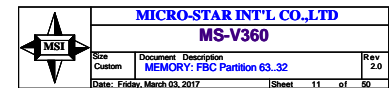


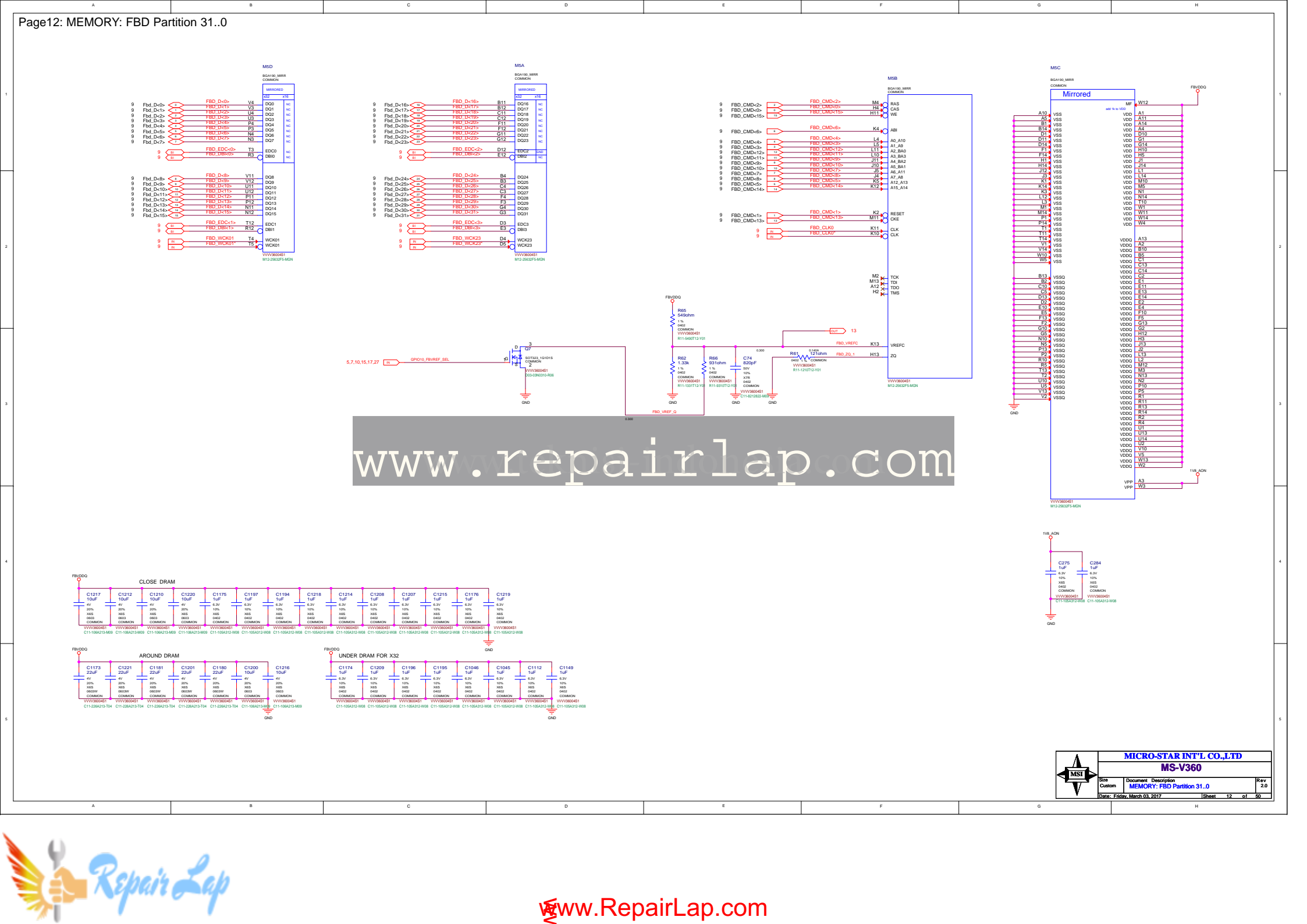
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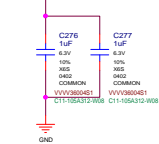
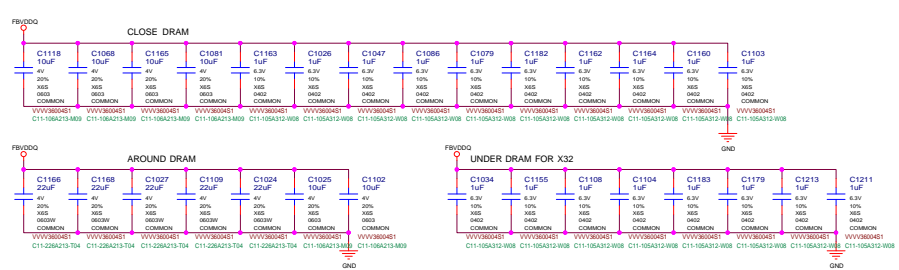
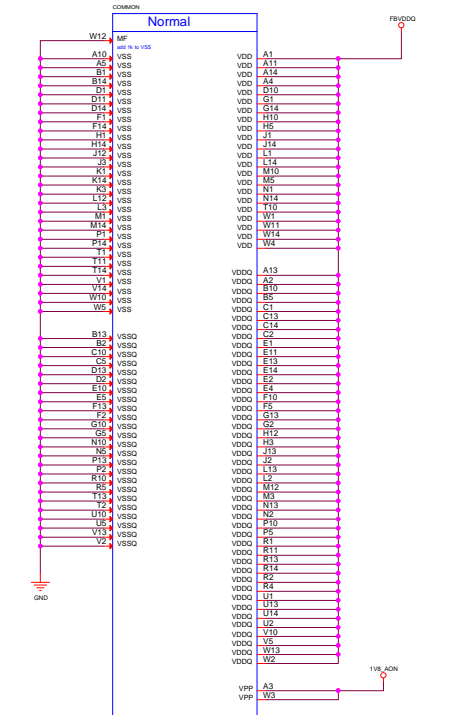
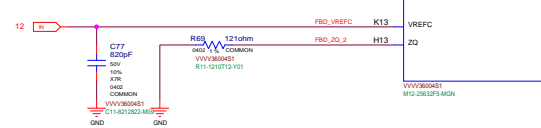
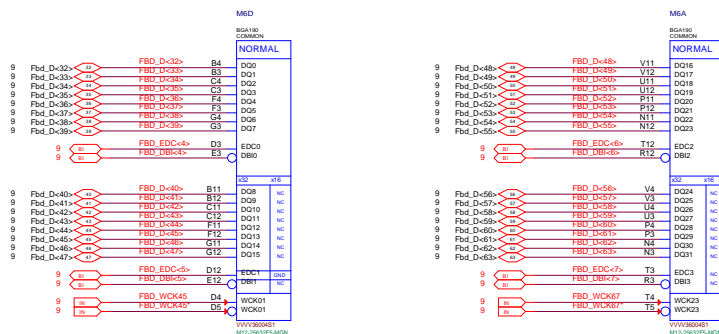


MICRO-STAR INT'L CO.,LTD			
MS-V360			
Size	Document	Description	Rev
Custom	Memory	Partition 31..0	2.0
Date: Friday, March 03, 2017	Sheet	10	of 50



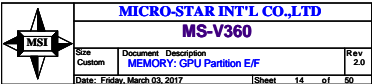


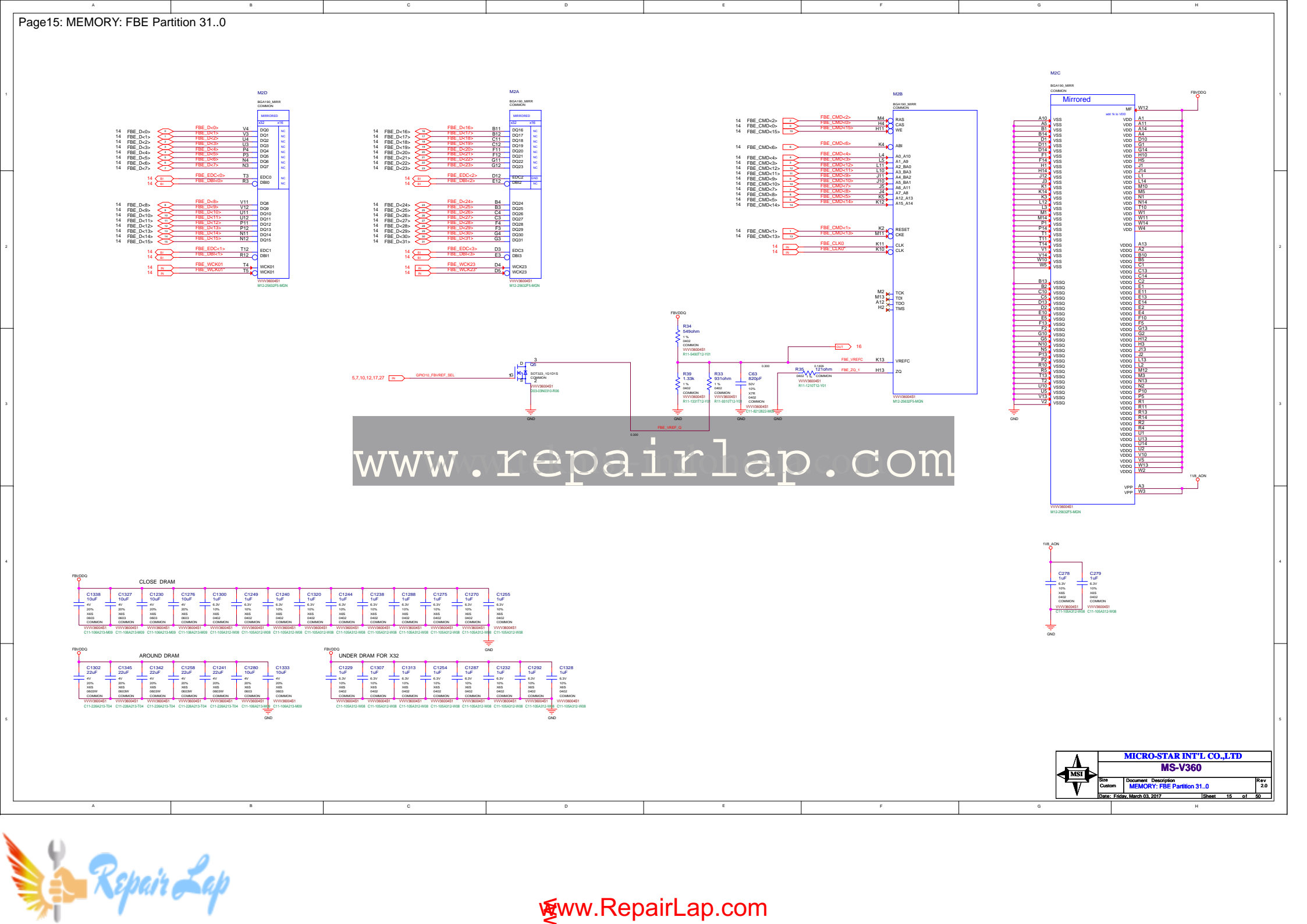


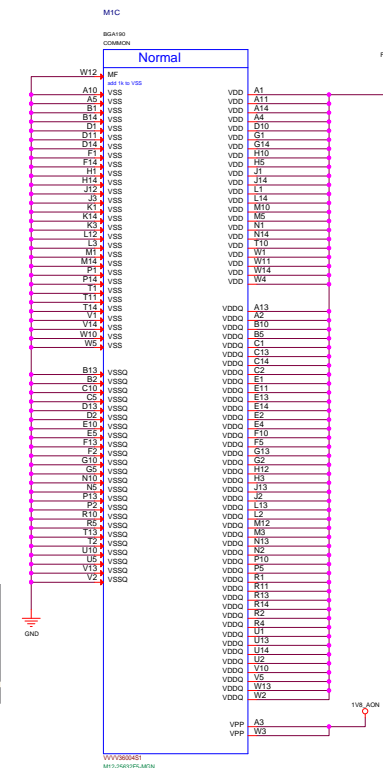
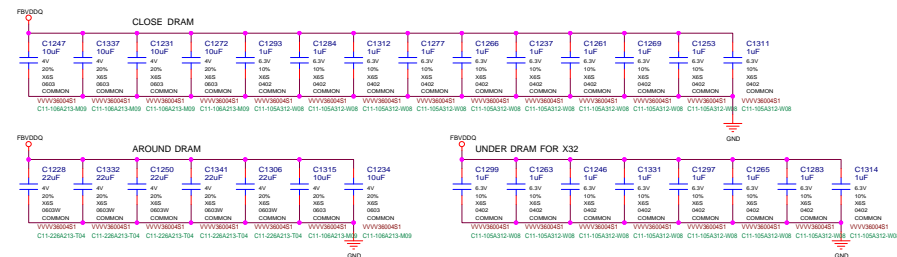


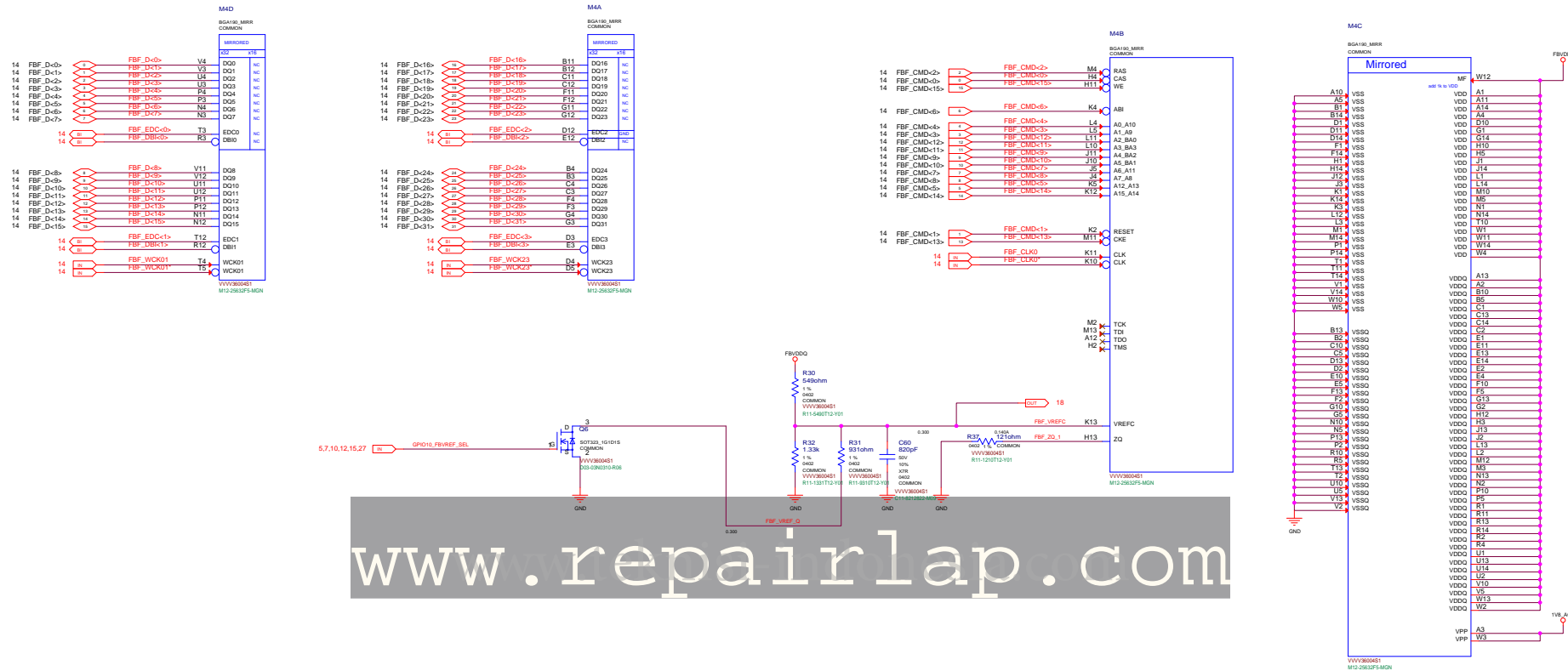
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	MS-V360		
	Size	Document Description	Rev
	Custom	MEMORY: FBD Partition 63..32	2.0
Date: Friday, March 03, 2017		Sheet	13 of 50



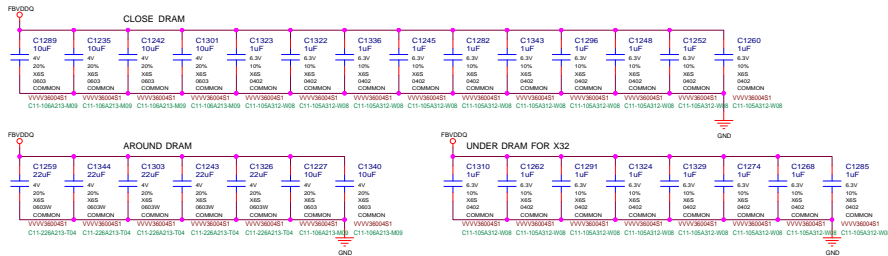


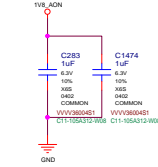
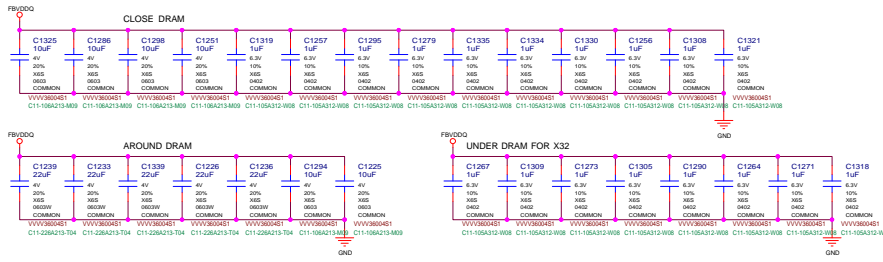
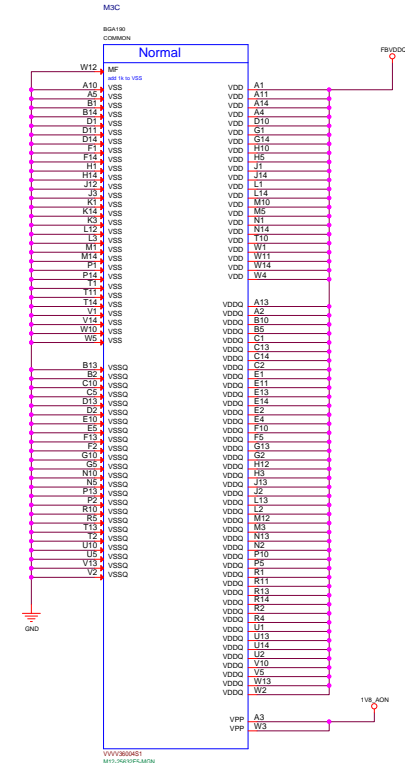
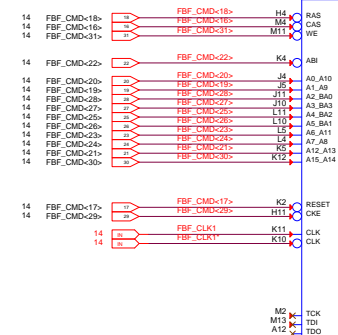
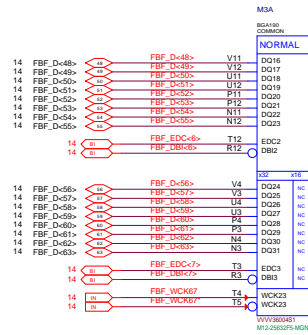
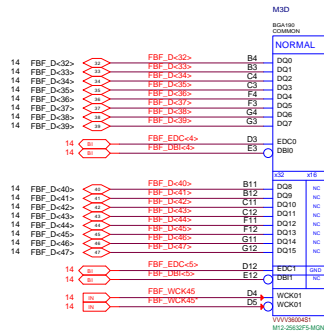


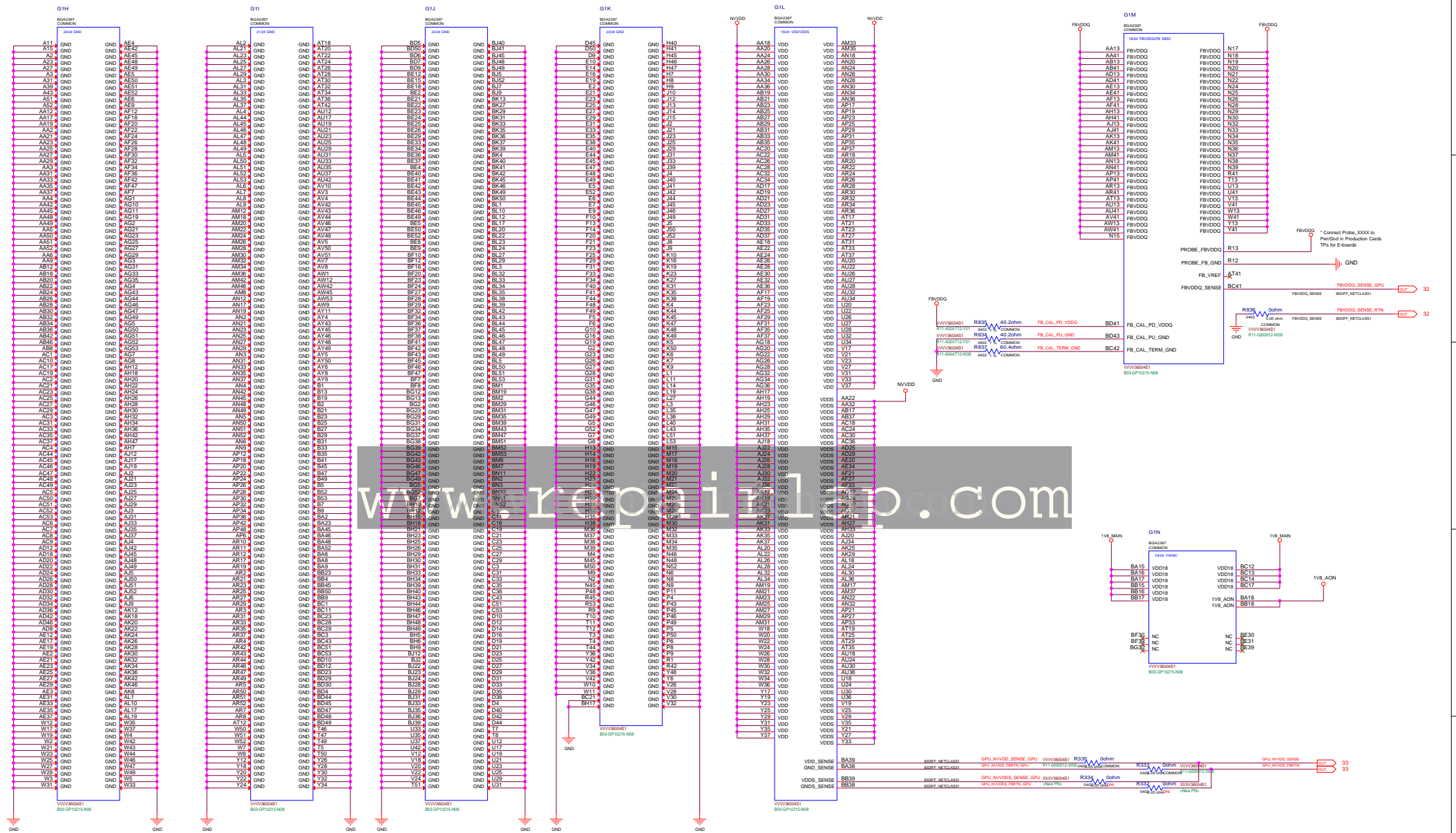


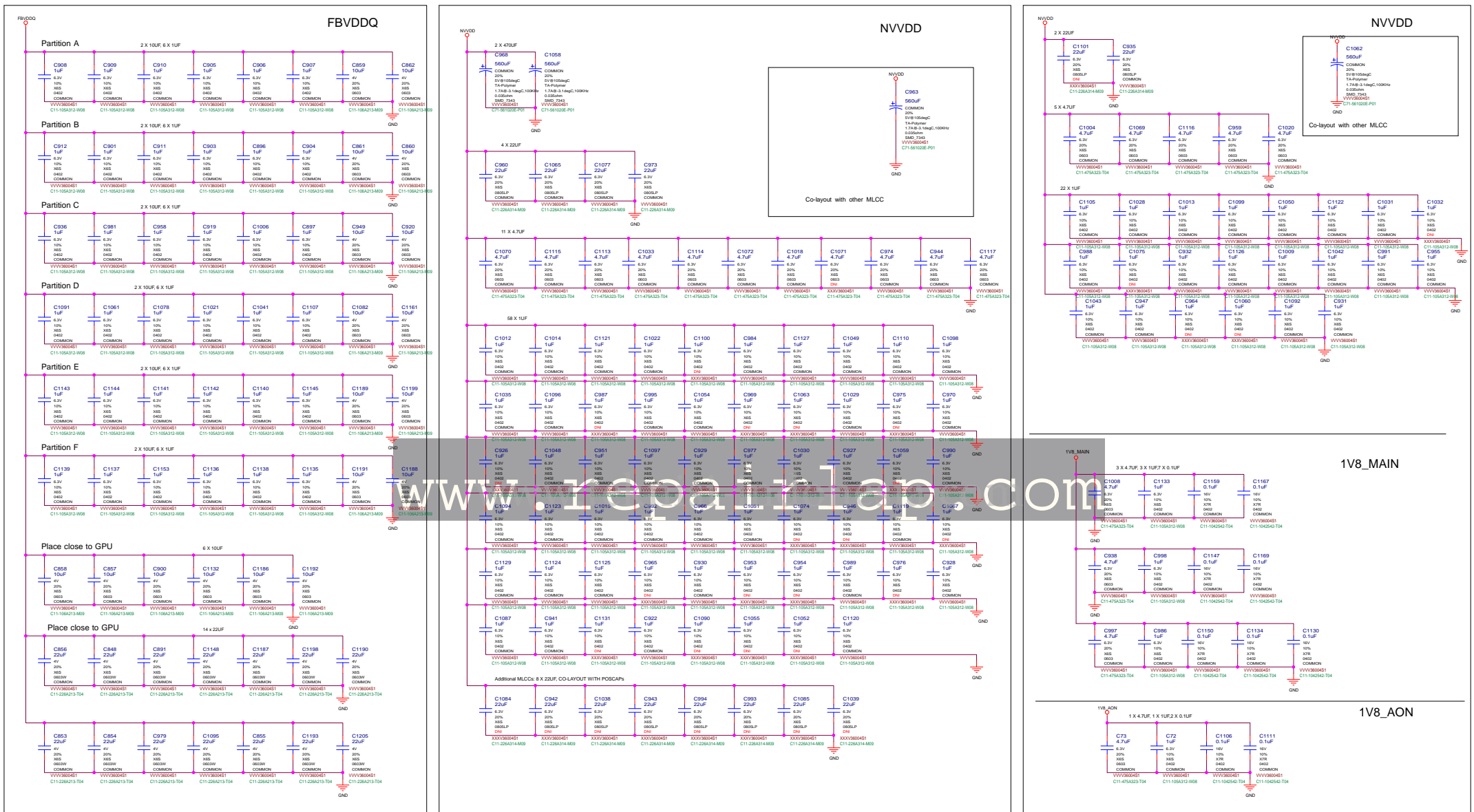


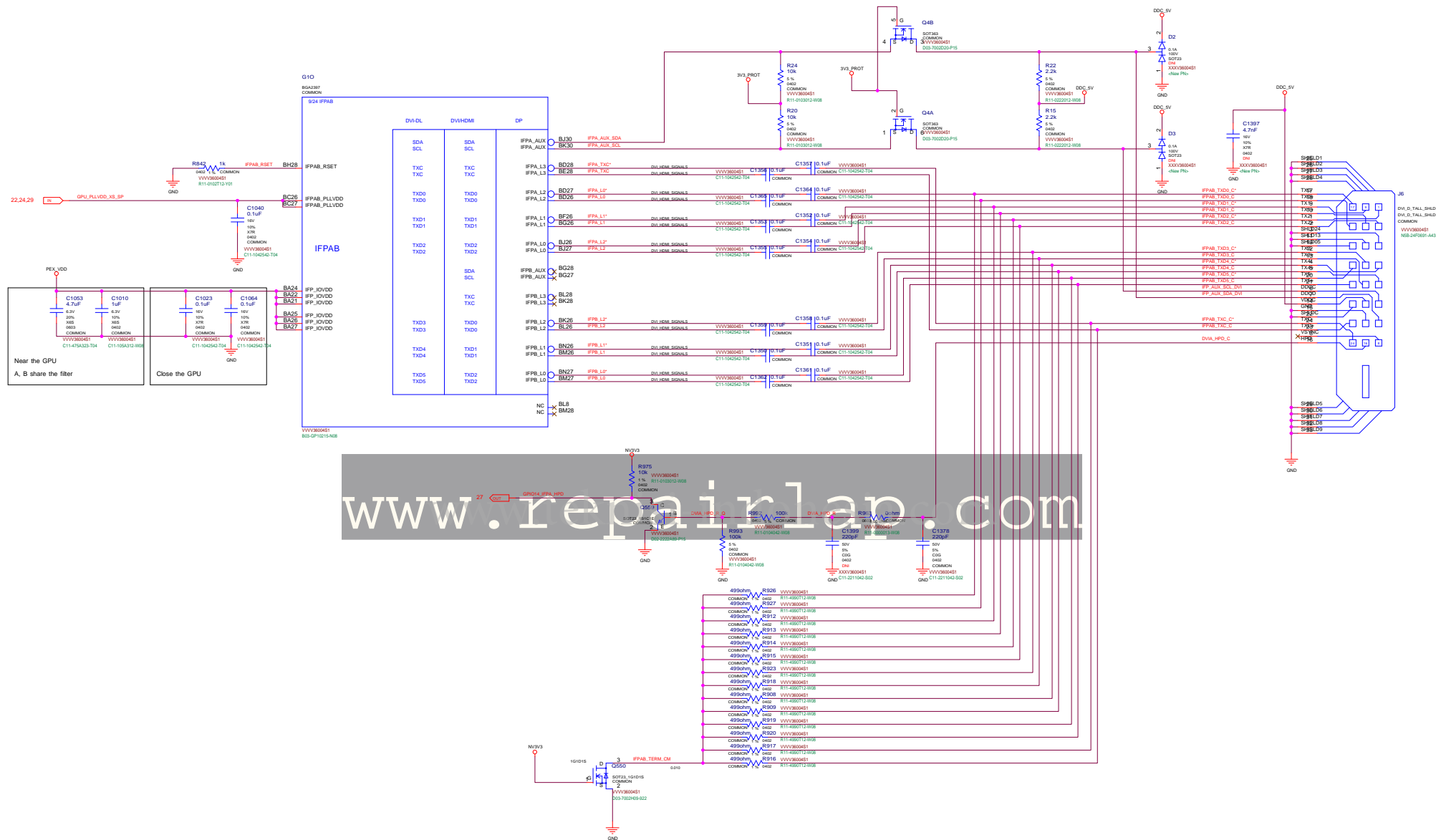
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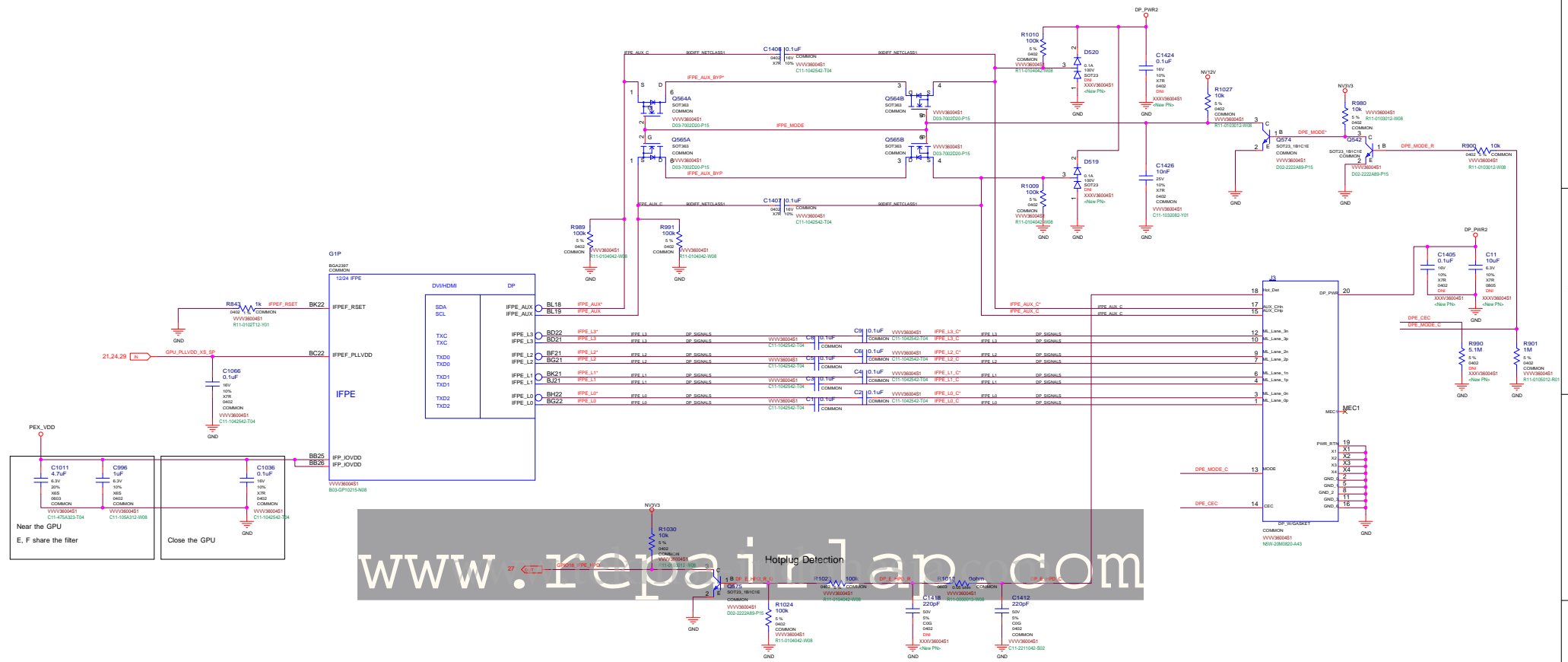


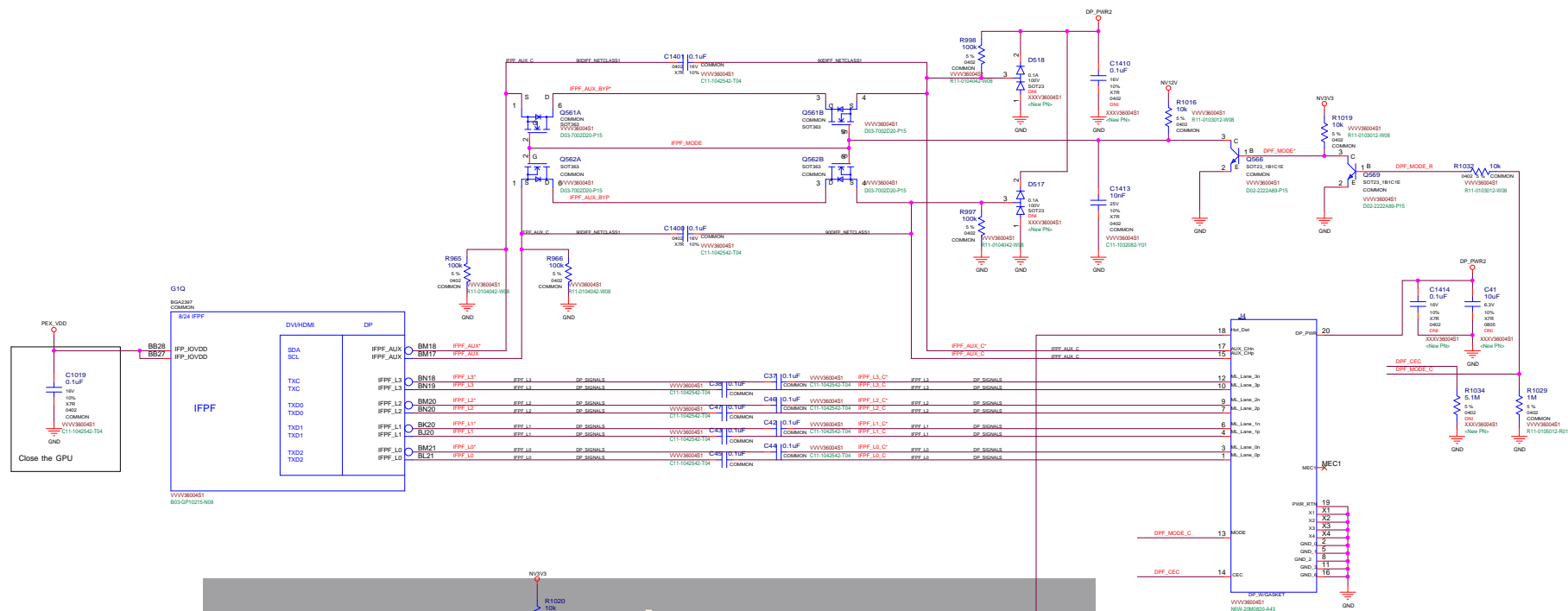




MICRO-STAR INT'L CO.,LTD			
MS-V360			
Size	Document	Description	Rev
Custom	IFPAB DVI-D-LD		2.0
Date: Friday, March 03, 2017		Sheet	21 of 50

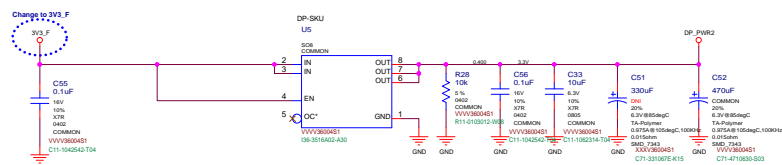






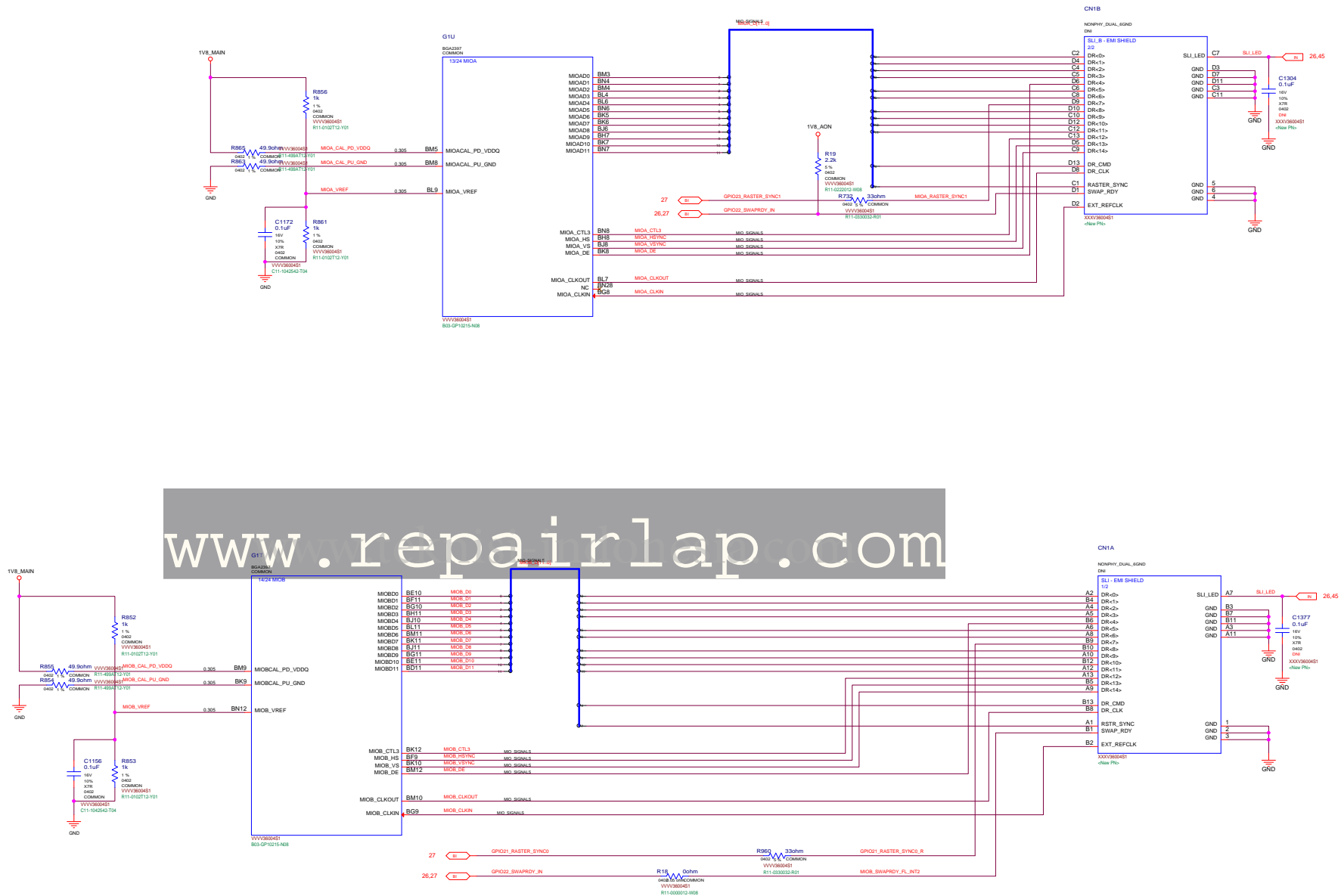
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Fused DP_PWR



MICRO-STAR INT'L CO.,LTD			
MS-V360			
Size	Document	Description	Rev
Custom	IFPF DP		2.0
Date: Friday, March 03, 2017	Sheet	23	of 50

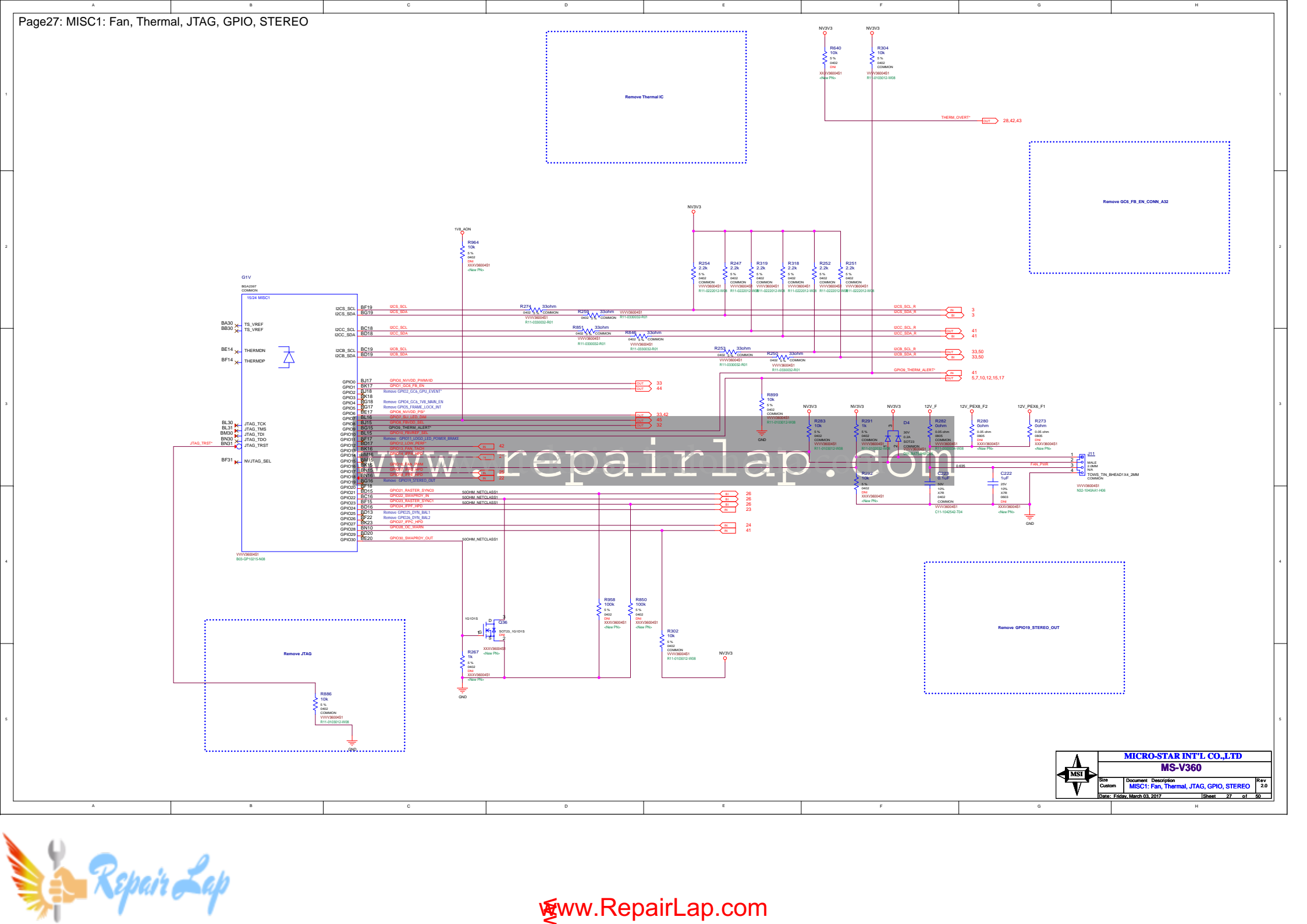
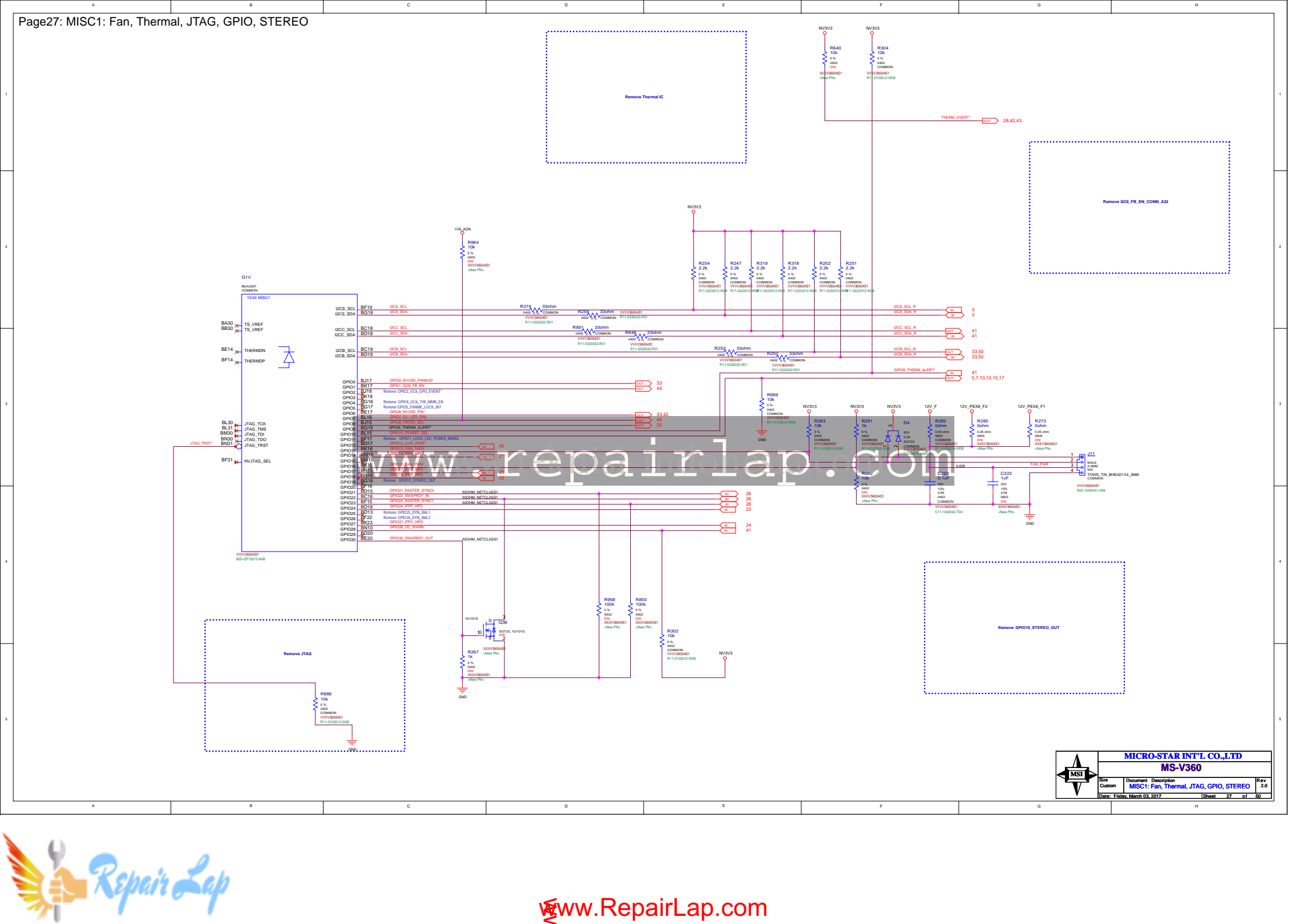




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MICRO-STAR INT'L CO.,LTD			
MS-V360			
Size	Document	Description	Rev
Custom	MIOA/B Interface		2.0
Date: Friday, March 03, 2017		Sheet	26 of 50





Page27: MISC1: Fan, Thermal, JTAG, GPIO, STEREO

[illegible]

STRAP2	STRAP1	STRAP0	RAMCFG[4:0]
L	L	L	00000
L	H	L	00010
L	H	H	00011
H	H	L	00110
H	H	H	00111
ROM_SO	ROM_SI	ROM_SCLK	SOR_EXPOSED[3:0]1:ENABLE 0:DISABLE
L	L	L	1111 DEFAULT SOR0/1/2/3 ENABLE
L	L	H	1110
L	H	L	1101
L	H	H	1100
H	L	L	1011
H	L	H	1010
H	H	L	1001
H	H	H	1000
L	L	M	0111
L	M	L	0110
L	M	H	0101
L	H	M	0100
H	L	M	0011
H	M	L	0010
H	M	H	0001
H	H	M	0000

STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	0	0
M	L	H	1	1	0	0
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1 DEFAULT
L	L	L	0	0	0	0

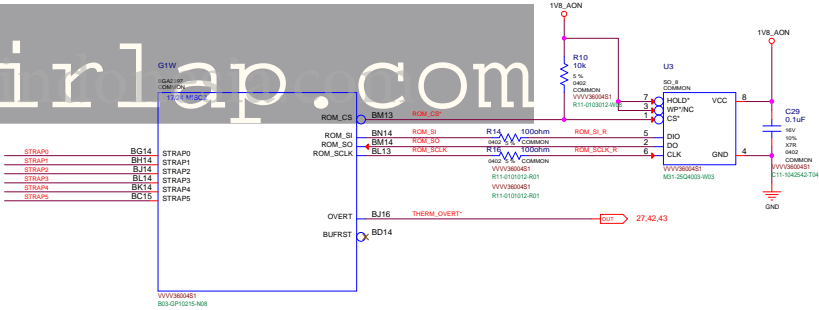
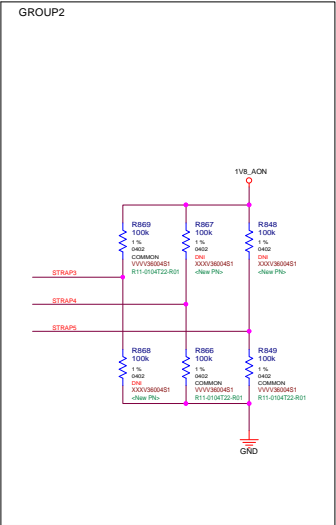
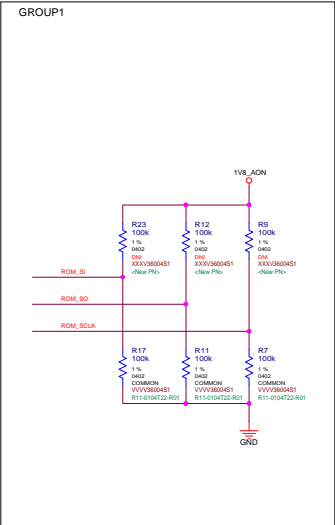
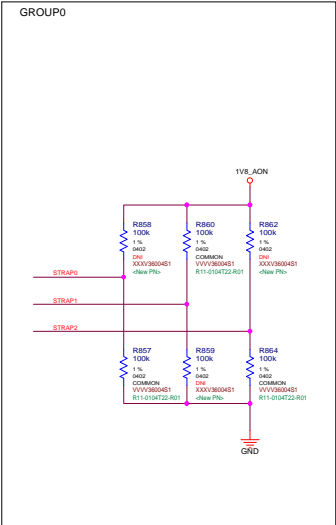
H=High :Tied to 1.8V
M=Middle:Tied to 0.9V
L=Low :Tied to 0V

1:SMB_ALT_ADDR ENABLE
0:SMB_ALT_ADDR DISABLE

1:DEVID_SEL REBRAND
0:DEVID_SEL ORIGINAL

1:PCIE_CFG LOW POWER
0:PCIE_CFG HIGH POWER

1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE

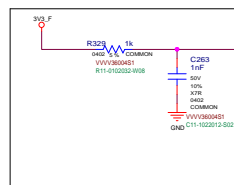




PEX PLL Switcher

$$V_{out} = V_{ref} * (1 + R_{top}/R_{bot})$$

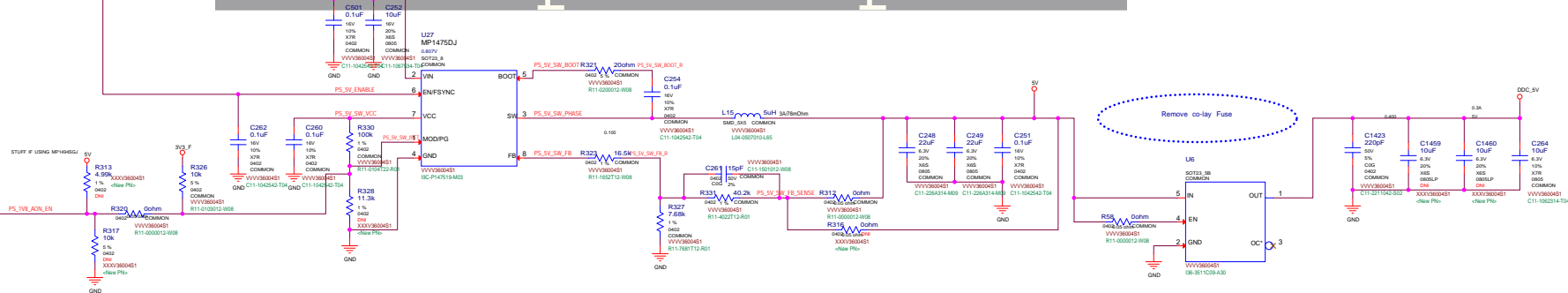
$$1.008 = 0.6 * (1 + 6.81K/10K)$$



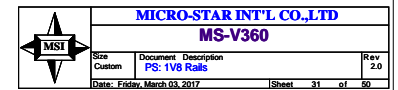
STUFF IF USING MP148GQJ

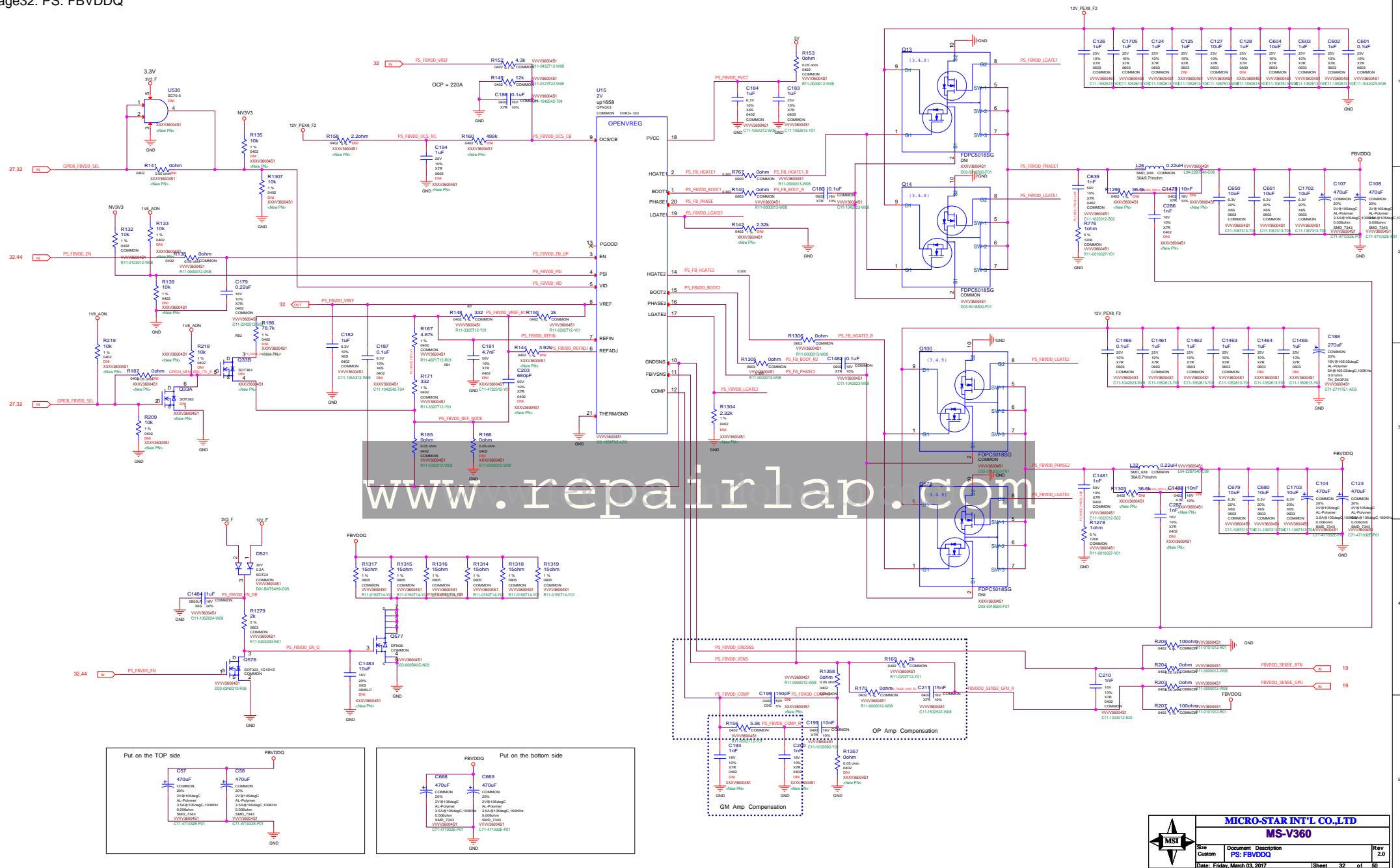
31.44,46

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MS-V360			
Size	Document	Description	Rev
Custom	PS: 5V, PEX_VDD		2.0
Date: Friday, March 03, 2017	Sheet	30	of 50





MICRO-STAR INT'L CO., LTD			
MS-V360			
Size	Document	Description	Rev
Custom	PS: FBVDDQ		2.0
Date: Friday, March 03, 2017	Sheet	32	of 50



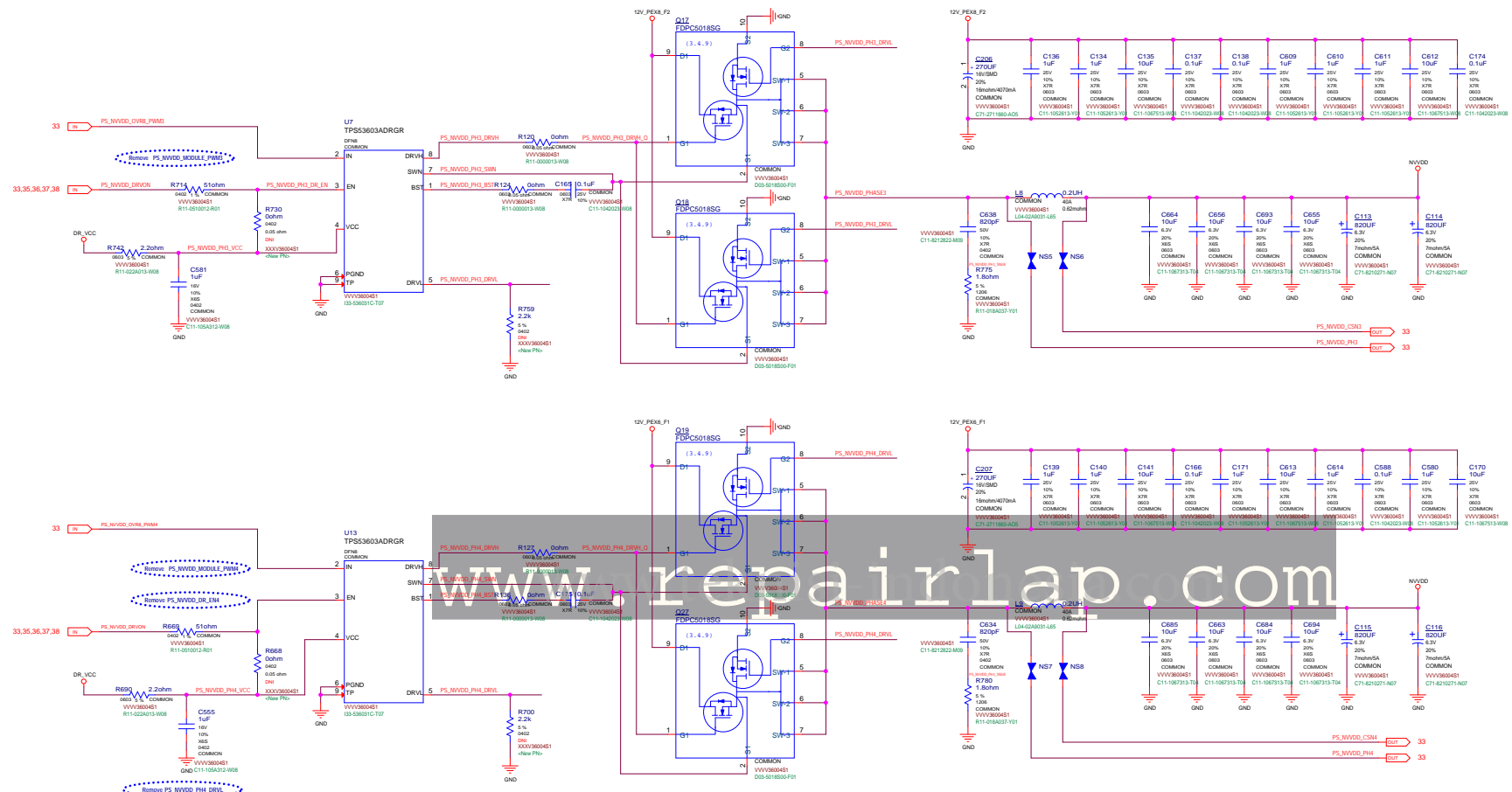



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	MS-V360		
	Size	Document Description	Rev
	Custom	PS: NVVDD CONTROLLER_PWR-MODULE	2.0
Date: Friday, March 03, 2017			Sheet 34 of 50









MICRO-STAR INT'L CO.,LTD
MS-V360

Size	Document	Description	Rev
Custom	PS: NVVDD Phase 3,4		2.0
Date: Friday, March 03, 2017			Sheet 36 of 50







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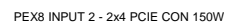
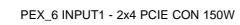
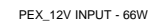
	MICRO-STAR INT'L CO.,LTD		
	MS-V360		
	Size	Document Description	Rev
	Custom	Dynamic Power Balance Phases	2.0
Date: Friday, March 03, 2017		Sheet	39 of 50



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	MICRO-STAR INT'L CO.,LTD		
	MS-V360		
	Size	Document Description	Rev
	Custom	PS: Dynamic Power Balance Logic	2.0
Date: Friday, March 03, 2017		Sheet	40 of 50



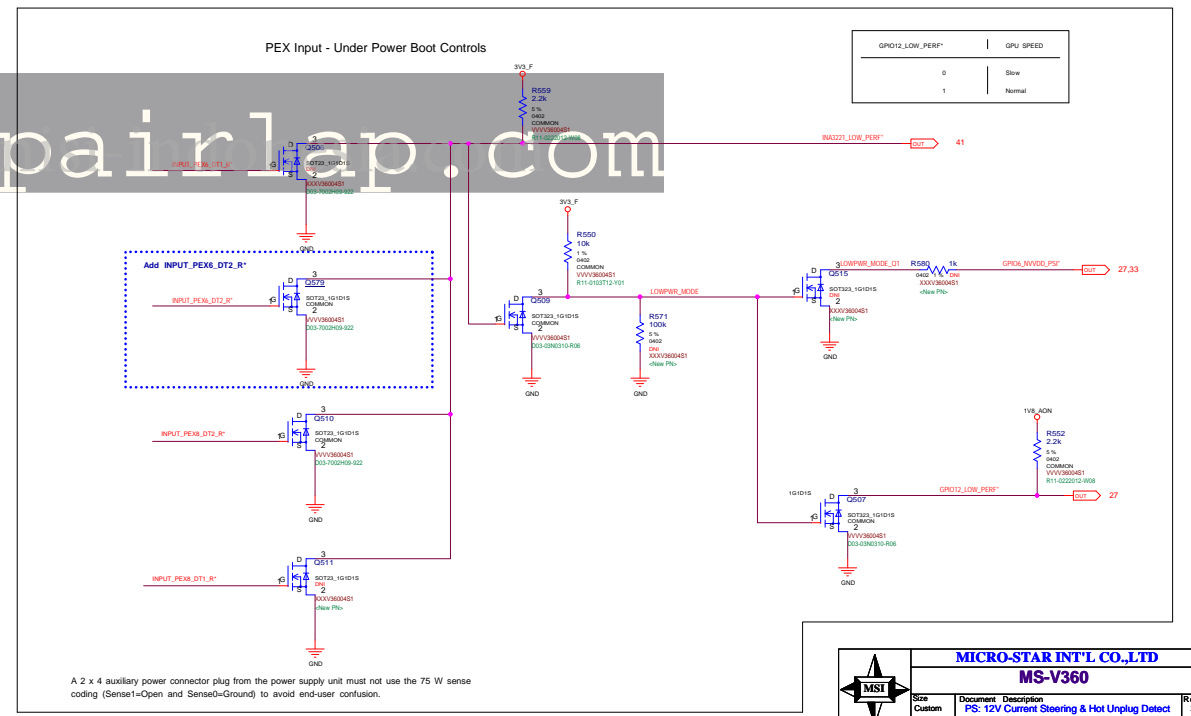
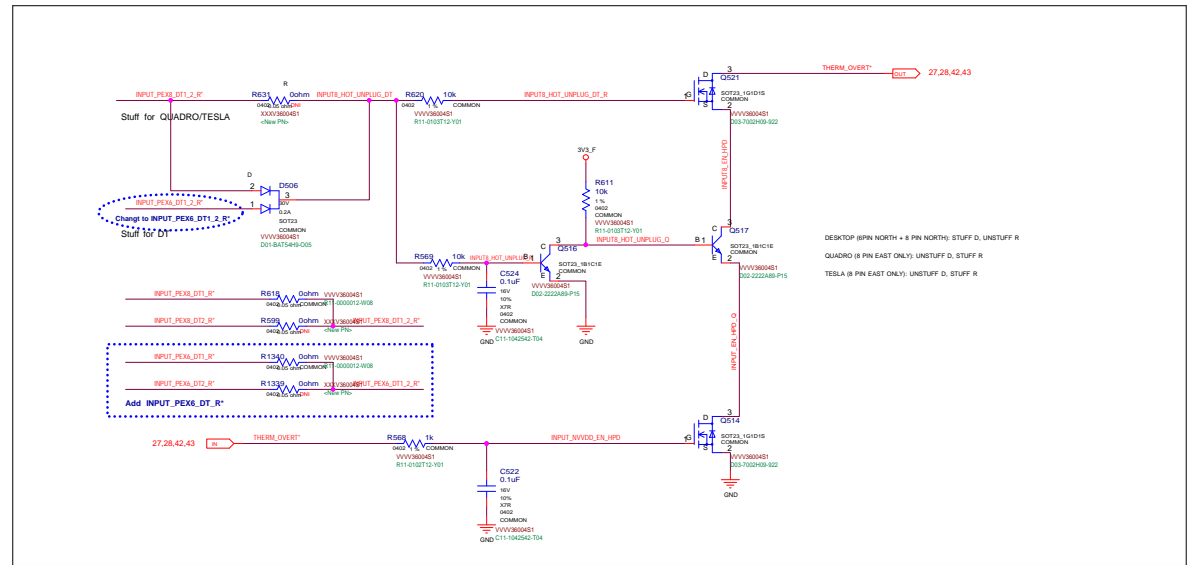
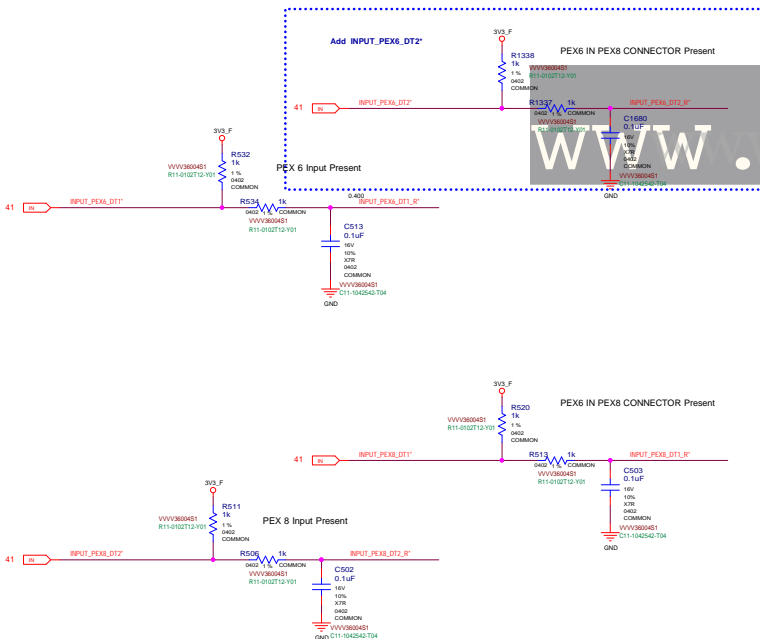
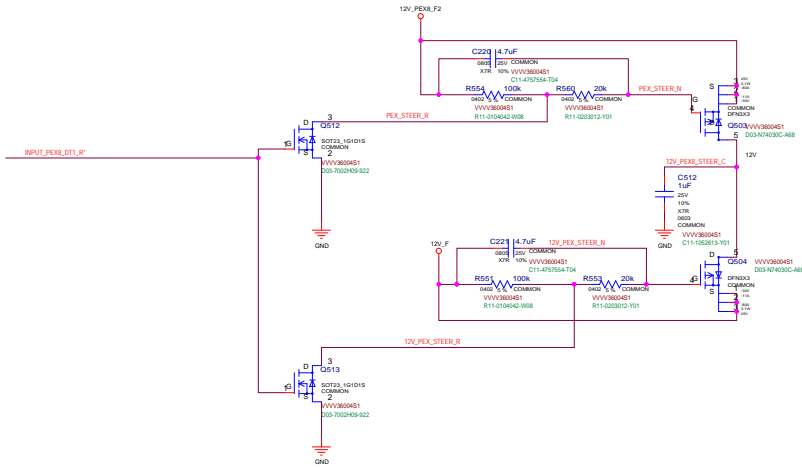


QUADRO/TESLA:

12V CURRENT STEERING (UNDER POWER BOOT):

GUIDES CURRENT FROM PEX EDGE TO PEX 8 PIN INPUT AREA

For OpenVeg Type4 + Phase Doubler, 2 phase PSI mode

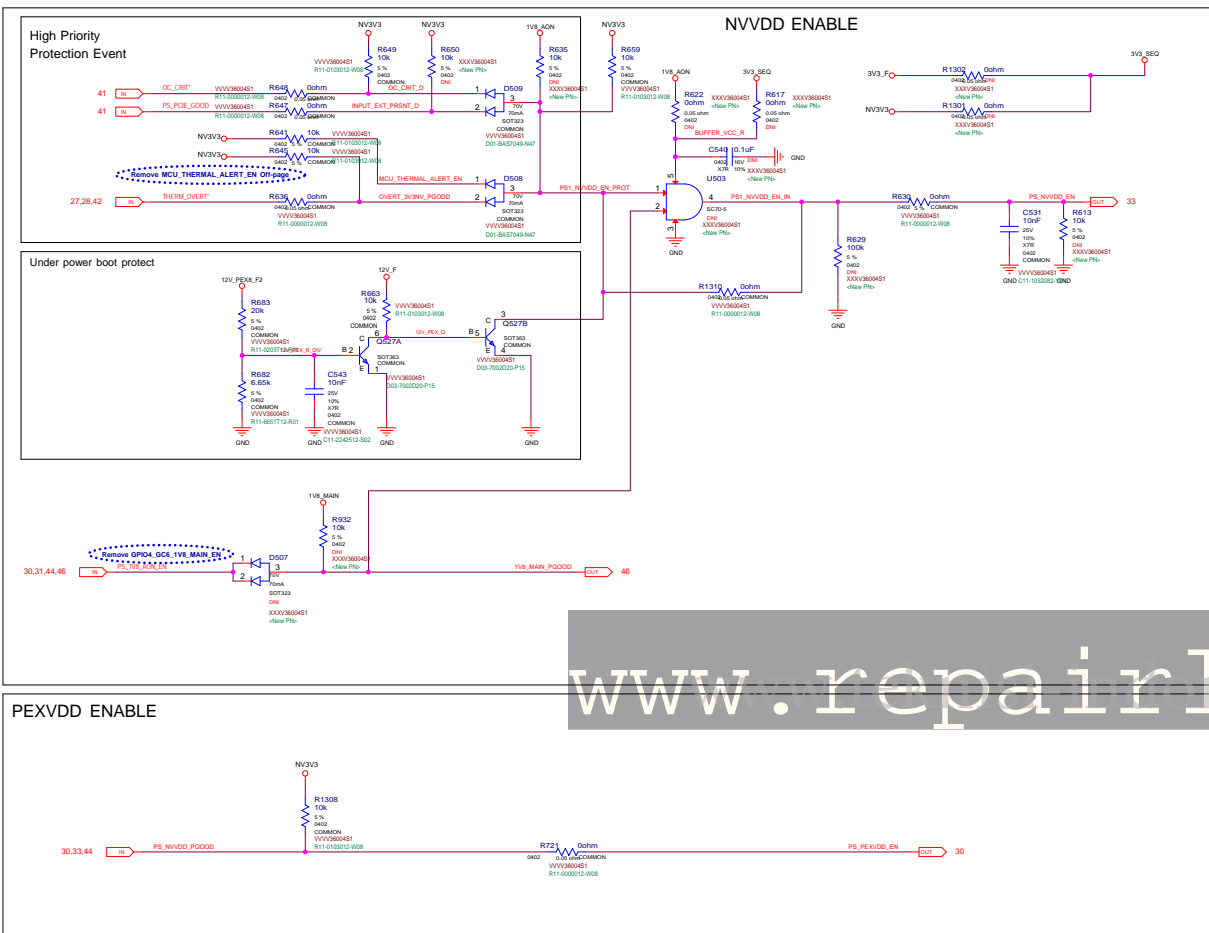


A 2 x 4 auxiliary power connector plug from the power supply unit must not use the 75 W sense coding (Sense1=Open and Sense0=Ground) to avoid end-user confusion.

GPU12_LOW_PERF*	GPU SPEED
0	Slow
1	Normal

MICRO-STAR INT'L CO.,LTD			
MS-V360			
Size	Document	Description	Rev
Custom	PS: 12V Current Steering & Hot Unplug Detect		2.0
Date: Friday, March 03, 2017	Sheet	42	of 50

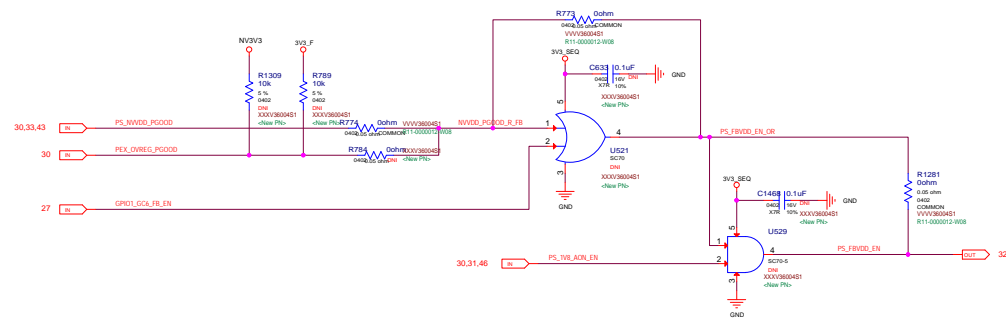




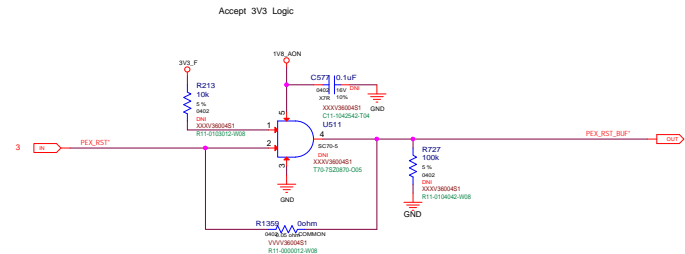
Remove PEX_CLKREQ*

Remove GPU_EVENT*

FBVDD/Q ENABLE



PEX_RST# LOGIC



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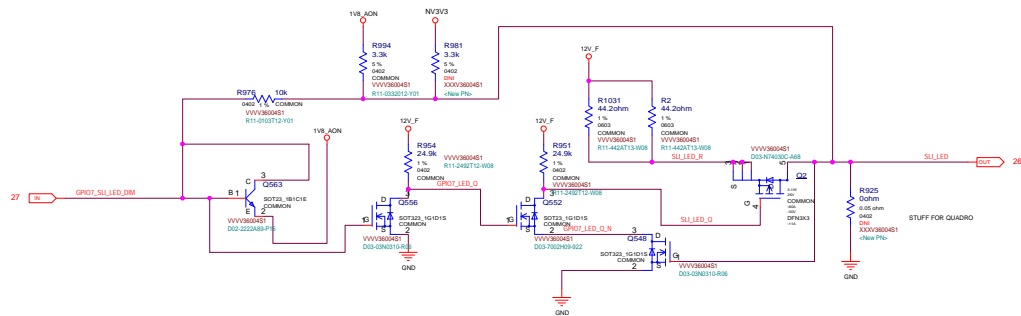
	MICRO-STAR INT'L CO.,LTD		
	MS-V360		
	Size	Document Description	Rev
	Custom	GC6 MISC	2.0
Date: Friday, March 03, 2017		Sheet	44 of 50



Remove LED HEADER(GEFORCE ONLY)

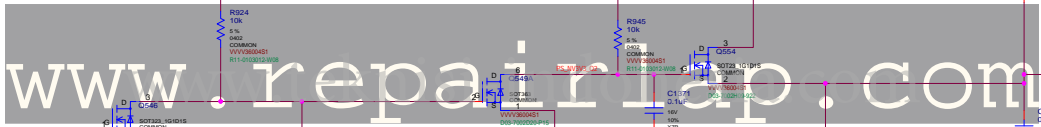
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SLI LED (GEFORCE ONLY)



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Size	Document	Description	Rev
Custom	GEFORCE LED & SLI LED		2.0
Date: Friday, March 03, 2017		Sheet	45 of 50



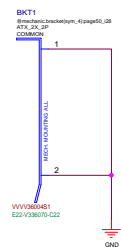


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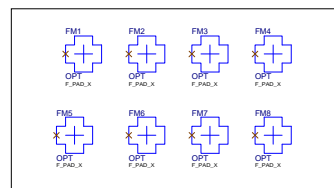
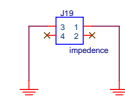
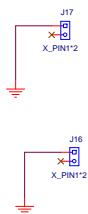
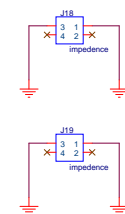
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	Size	Document Description	Rev
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Date: Friday, March 03, 2017			Sheet 47 of 50



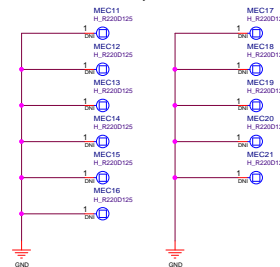
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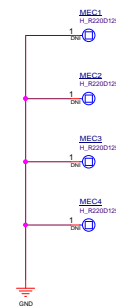
Screw



Mechanical Holes Symbol



GPU HOLES SYMBOL



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Size	Document	Description	Rev
Custom	Mechanical	Bracket/Thermal Solution	2.0
Date: Friday, March 03, 2017		Sheet	48 of 50



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	MICRO-STAR INT'L CO.,LTD		
	MS-V360		
	Size	Document Description	Rev
	Custom	VR THERMAL PROTECTION	2.0
Date: Friday, March 03, 2017			Sheet 49 of 50



